

Docket No.: 064965-0168



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Qi XIANG

RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE

Application No.: 10/620,605

Customer No.: 20277
Confirmation No.: 9283

Filed: July 17, 2003

Group Art Unit: 2811

Examiner: Sara W. Crane

Title: CMOS WITH STRAINED SILICON CHANNEL NMOS AND SILICON
GERMANIUM CHANNEL PMOS

REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

These remarks are filed in response to the final Office Action dated November 16, 2005,
pursuant to 37 C.F.R. § 1.116.

REMARKS

This Request for Reconsideration is filed in response to the Office Action dated
November 16, 2005. For the following reasons this application should be allowed and the case
passed to issue.

Claims 10-27 are pending in this application. Claims 1-9 have been canceled. Claims
10-27 are rejected.

Claim Rejections Under 35 U.S.C. § 112

Claims 10-27 are rejected under 35 U.S.C. § 112, second paragraph, as being indefinite.
This Examiner alleged that "interposed isolation regions" is not clear. The Examiner questioned